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Patricia Muir

Any fee due as a result of this paper, not covered by an enclosed check, may be charged on Deposit Acct. No. 50-1290.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:

Yoshihisa MATSUBARA et al.

Serial No.:

09/840,386

Filed:

April 23, 2001

Title:

METHOD FOR MANUFACTURING SEMICONDUCTOR

DEVICE, AND PROCESSING SYSTEM AND

SEMICONDUCTOR DEVICE

Examiner:

Lan Vinh

Group Art Unit:

1765

Confirmation No.:

2510

Attorney Docket No.: 100806-00199 (NEKA 18.612)

November 28, 2005

Mail Stop AF Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

NOTICE OF APPEAL

Sir:

Applicants hereby appeal to the Board of Appeal from the final rejection dated June 28, 05, of non-allowed claims 1,2,13-16, and 18

1. (x) An extension of time to respond to the final rejection is requested for <u>two (2)</u> month(s).

2. (x) A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.

Any fee due with this paper may be charged to Deposit Account 50-1290.

Respectfully submitted,

Brian E. Hennessey Reg. No. 51,271

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BEH:pm